

Electronic Acknowledgement Receipt

EFS ID:	1085887
Application Number:	10777992
Confirmation Number:	3317
Title of Invention:	Method and circuit for allocating memory arrangement addresses
First Named Inventor:	Georg Braun
Customer Number:	46798
Filer:	Gero G. McClellan/Joseph Jong
Filer Authorized By:	Gero G. McClellan
Attorney Docket Number:	INF 2166-US
Receipt Date:	20-JUN-2006
Filing Date:	12-FEB-2004
Time Stamp:	21:50:33
Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment - After Non-Final Rejection	WB0059_ROA.pdf	390047	no	10

Warnings:**Information:****Total Files Size (in bytes):**

390047

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.